

isc P-Channel MOSFET Transistor

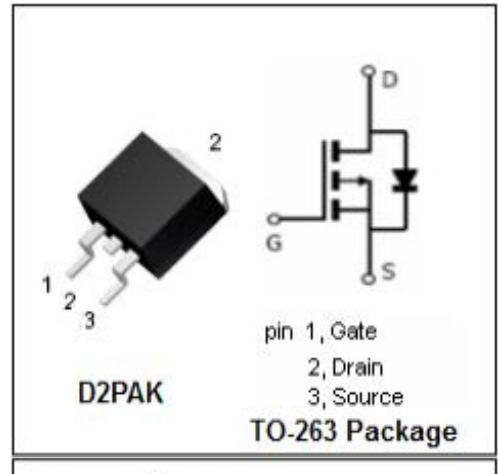
IRF4905S

• FEATURES

- Static drain-source on-resistance:
 $R_{DS(on)} \leq 20m\Omega$ (@ $V_{GS} = -10V$; $I_D = -42A$)
- Advanced trench process technology
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

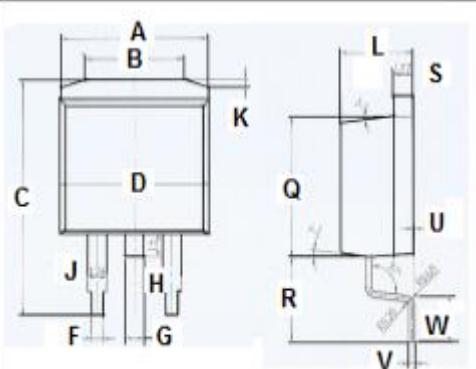
• APPLICATIONS

- Fast switching application.



• ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DSS}	Drain-Source Voltage	-55	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current-Continuous	-70	A
P_D	Total Dissipation @ $T_c=25^\circ C$	170	W
T_J	Max. Operating Junction Temperature	-55~150	°C
T_{stg}	Storage Temperature	-55~150	°C



DIM	mm	
	MIN	MAX
A	10	
B	6.6	6.8
C	15.23	15.25
D	10.15	10.17
F	0.76	0.78
G	1.26	1.28
H	1.4	1.6
J	1.33	1.35
K	0.4	0.6
L	4.6	4.8
Q	8.69	8.71
R	5.28	5.30
S	1.26	1.28
U	0.0	0.2
V	0.37	0.39
W	2.80	2.82

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(j-c)}$	Channel-to-case thermal resistance	0.75	°C/W

isc P-Channel MOSFET Transistor**IRF4905S****ELECTRICAL CHARACTERISTICS** $T_c=25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}; I_{\text{D}}= -250 \mu\text{A}$	-55			V
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}; I_{\text{D}}= -250 \mu\text{A}$	-2		-4	V
$R_{\text{DS(on)}}$	Drain-Source On-Resistance	$V_{\text{GS}}= -10\text{V}; I_{\text{D}}= -42\text{A}$			20	$\text{m}\Omega$
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}= \pm 20\text{V}; V_{\text{DS}}= 0\text{V}$			± 100	nA
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}= -55\text{V}; V_{\text{GS}}= 0\text{V}$			-25	μA
V_{SD}	Diode forward voltage	$I_{\text{S}}= -42\text{A}, V_{\text{GS}} = 0\text{V}$			-1.3	V

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